DI WU

Personal Information	666 Spadina Ave. Room 1607 Toronto, ON M5S 2H8, Canada Phone: +1 (647) 293 8666 E-mail: peterwudi.wu@utoronto.ca Webpage: http://individual.utoronto.ca/peterwudi/		
Profile	Highly-motivated and reliable graduate student with solid technical skills; strong knowledge and interests in computer architectures and FPGA-based accelerators; rich experiences in hardware and software development; have positive work attitude and able to work well in a team		
Education	University of Toronto, Toronto, Ontario, Canada M.A.Sc., Electrical and Computer Engineering, 2012 - 2014 Thesis: "High Performance Branch Predictors for Soft Processors" Supervisor: Prof. Andreas Moshovos		
	B.A.Sc. with honors, Electrical and Computer Engineering, Overall % average: $90.7/100$ 2007 - 2012 Cumulative GPA: $3.85/4.0$ 4_{th} year milestone project: Horizontal-Microcoded Soft-Processor		
Industrial Experience	Backup and Recovery (BAR) & Data Protection Services (DPS) developer May 2010 - August 2011 IBM DB2 LUW Kernel New Development (ND) 16-month Internship at IBM Toronto Software Lab, Toronto, Ontario, Canada		
	• Took a lead role in the design and implementation of a new DB2 feature, a standalone tool (db2adutl) for remote backup image and log archival to offload the Tivoli Storage Manager (TSM) and lower the impact on the DB2 server		
	• Preserved the availability to DB2's powerful automatic recovery tools with the new feature		
	• Designed and implemented backup safeguard code to detect Space Map Page (SMP) corruption for Database Managed Spaces (DMS)		
	• Empowered another standalone tool (db2ckbkp) to perform SMP validation and ensure backward compatibility		
	• Measured and analyzed the decreasing performance of test buckets and identified a flaw in the design of Buffer Pool Services (BPS) that potentially contributed to the falling of test performance		
	AFC Clearing Center software developerMay - August 2009Automated Fare Collection System (AFC) developmentSummer Internship at Panda Electronics, Nanjing, Jiangsu, China		
	• Independently designed and developed a middleware applied on Nanjing Urban Rail Transport Automated Fare Collection System (AFC) and AFC Clearing Center (ACC) to ensure fast and accurate inter-process communication between multiple gates and ACC		
	• Optimized the configuration of shared memory of the system		
Publications Peer-Reviewed Conferences	[C1] Di Wu and Andreas Moshovos, "Advanced Branch Predictors for Soft Processors", In proceedings of the International Conference on ReConFigurable Computing and FPGAs (ReConFig'14) (to appear), December 2014.		

	[C2] Goran Narancic, Patrick Judd, Di Wu, Islam Atta, Michel El Nacouzi, Jase Enright Jerger, Serag Gadelrab, Kyros Kutulakos, Andreas Moshovos and "Evaluating the Memory System Behavior of Smartphone Workloads", In Proceed International Conference on Embedded Computer Systems: Architectures Simulation (SAMOS'14), July 2014	on Zebchuk, Natalie Jorge Albericio. edings of the 5, Modeling and	
	[C3] Di Wu, Kaveh Aasaraai and Andreas Moshovos, "Low-cost, High-performan Soft Processors", In proceedings of the 23rd International Conference on Fiel and Applications (FPL'13), September 2013	nce Branch Predictors for Id Programmable Logic	
Publications Refereed Posters	[P1] Di Wu and Andreas Moshovos, "Image Signal Processors for FPGAs", In pro IEEE International Symposium on Field-Programmable Custom Computing May 2014.	oceedings of The 22nd g Machines (FCCM'14),	
Technical Skills	 High proficiency and large industrial software development experience using C/C++ on Linux/AIX/Windows platforms 		
	 Solid understanding of computer architectures, including general purpose processors and application specific accelerators 		
	 Highly experienced hardware development and debugging skills using Verilog HDL for Altera FPGAs 		
	Functional/timing simulator experiences including Simplescalar, Marss, Simics and Flexus		
	 Programming Languages: C/C++, Verilog HDL, Assembly, Perl, and some e XML and SQL 	xperience with Bash,	
Scholarships	University of Toronto Fellowship	2012-2014	
and Awards	• University of Toronto Research Assistantship (AENAO group)	2012-2014	
	University of Toronto Scholar Award	2007 and 2009	
	• The Faculty of Applied Science and Engineering Dean's Honour List	2007-2012	
Teaching Assistantships	• ECE243S - Computer Organization Winter A second year course on assembly language and computer organization	243S - Computer Organization Winter 2013 and Winter 2014 cond year course on assembly language and computer organization	
	• ECE352F - Computer Organization A third year course on assembly language and computer organization	Fall 2013	
Languages	ENGLISH · Full professional proficiency		
	CHINESE Native proficiency		

September 30, 2014